Lab 3

20 February, 2013

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Objective-

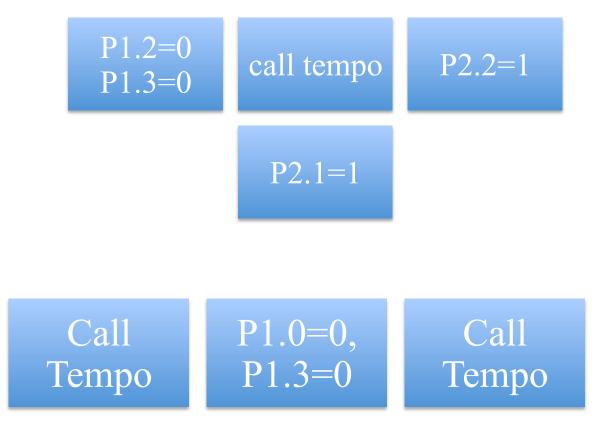
The objective of this lab was to become familiar using external I/O devices connected to P0, P1, P2, and P3 by programming traffic lights into the LEDs connected to P1.

Equipment used-

Software: a text editor and an 8051 ASM assembler A step debugger that can be used to execute a program one step at a time Register, code memory, data memory, and input/output port

contents are displayed to aid debugging.

Flow Chart-



P1.0=0,	Call	Mov p1,
P1.5=0	Tempo	#0DEH
Call	Move p1,	call
Tempo	#)f6h	tempo



Test Results-Urgent on and off

🛃 EdSim51DI - Version 2.1.5 AOS312lab3.asm	×
System Clock (MHz) 12.0 5 VDdate Freq.	RST Step Run New Load Save Copy Paste P0.6 1 Display-select Decoder CS/DAC MR Time: 12lus - Instructions: 90 0 0 70.5 1 Reyrad Column 1 +
R/O W/O TH0 TL0 R7 0x00 B 0x00 0x00 0x00 0x00 0x00 R6 0x1B ACC 0x00	Image: Weight of the second
RXD TXD R5 0x00 PSW 0x00 1 1 TMDD 0x00 R4 0x1B IP 0x00	P0.2 1 Keypad Row 2 ØØ14 CALL TEMPO P0.1 1 Keypad Row 1 P0.0 1 Keypad Row 0 P0.1 1
SCON 0x00 TCON 0x00 R3 0x00 IB 0x00 R2 0x1B PCON 0x00 0x00 <th>P1.7 1 LED 7 Seg. dplDAC DB7 LCD DB7 P1.6 1 LED 6 Seg.glDAC DB6 LCD DB6</th>	P1.7 1 LED 7 Seg. dplDAC DB7 LCD DB7 P1.6 1 LED 6 Seg.glDAC DB6 LCD DB6
pins bits TH1 TL1 R1 0x00 DPH 0x00 0xFF 0xFF P3 0x00 0x00 R0 0x1B DPL 0x00	ØØ16 L2: MOV P1, #ØDEH P1.4 1 LED 4[Seg. e]DAC DB4[LCD DB4 P1.3 0 LED 3[d]DB3[DB3].
OXFF OXFFP2 PC 8051 SP 0x11 OXF6 OXF6P1 OXF0025 ACC 0000 0000	ØØ19 CALL TEMPO P1.2 1 LED 21 (L.D. D21 DD21LCD D5 P1.1 1 LED 11Seg. b1DAC DB1LCD DB1 P1.0 0 LED 01Seg. a1DAC DB01LCD DB0
Modify RAM	ØØ1B MOV P1,#ØEEH P2.7 1 SW 71ADC DB7 P2.6 1 SW 61ADC DB6
Data Memory addr 0x00 0x00 0yllue 0 1 2 3 4 5 6 7 8 9 A B C D E F	ØØIE CALL TEMPO P2.4 1 SW 41ADC DB4 P2.3 1 SW 31ADC DB3
00 18 00 18 00 18 00 18 00 0.0 0.0 3.00 3.00 3.00 3.00 3.00 3.	ØØ2Ø JMP start P2.2 1 SW 2/ADC DB2 ;Mov pl, #Ø P2.1 SW 1/ADC DB1
30 35 00 35 00 35 00 35 00 35 00 35 00 35 00 35 00 35 00 40 35 00 35 00 35 00 35 00 35 00 35 00 35 00 35 00 35 00	F3.7 1 ADC RD[Comparator Output ///DELAY FUNCTION F3.6 1 ADC WR F3.5 1 HORY Sensor 4
50 35 00 35 00 35 00 35 00 35 00 35 00 35 00 16 00 35 00 60 35 00 35 00 35 00 35 00 35 00 35 00 35 00 35 00 35 00	ØØ22 TEMPO: NOP P3.4 1 Display-select Input 1 ØØ23 NOP P3.3 1 AND Gate Output[Display-set 0
70 35 00 35<	ØØ241 NOP P3.2 1 ADC INTR ØØ251 NOP P3.1 1 Motor Control Bit 1/Est. UART Fac
DI / LD 1 2 3 AND Gate Dis	
4 5 6 Key Bounce Di	
7 8 9 7 6 5 4 3 2 1 0 # Standard	
4.82 V output	MIN Motor Enabled
DAC Error1 Function set not called. /	
S EdSim51DI - Version 2.1.5 AOS312lab3.asm	×
System Clock (MHz) 12.0 5 VDpdate Freq.	Time: 139us - Instructions: 105 U P0 5 1 Keyned Column 1
R/O W/O TH0 TL0 R7 0x00 0x00 0x00 0x00 0x00 0x00 R6 0x1B ACC 0x00	
RXD TXD R5 0x00 PSW 0x00 1 1 TMOD 0x00 R4 0x1E IP 0x00	ØØ16 L2: MOV P1, #ØDEH P0.1 Keypad Row 1 P0.1 Keypad Row 1 P0.0 Keypad Row 0
SCON 0x00 TCON 0x00 R3 0x00 IE 0x00 R2 0x1B PCON 0x00 0x00 <td>ØØ19 CALL TEMPO P1.7 1 LED 7 Seg. dp[DAC DB7 LCD DB7 P1.6 1 LED 6 Seg. g1DAC DB6 LCD DB6</td>	ØØ19 CALL TEMPO P1.7 1 LED 7 Seg. dp[DAC DB7 LCD DB7 P1.6 1 LED 6 Seg. g1DAC DB6 LCD DB6
pins bits TH1 TL1 R1 Ox00 DPH Ox00 0xFF 0xFF 0x0F 0x00 0x00 R0 0x1B DPL 0x00	ØØ1B MOV P1, #ØEEH P1.8] LED \$15eg. (IDAC DB\$1LCD DB\$ ± P1.4] LED \$15eg. (IDAC DB\$1LCD DB\$ P1.4] LED \$15eg. (IDAC DB\$1LCD DB\$ P1.5] LED \$15eg. (IDAC DB\$1.CD DB\$
OXFF OXFF P2 PC 8051 SP 0x11	ØØ1E CALL TEMPO P1.2 1 LED 2] ejDB2 LCD E P1.1 1 LED 1[Seq. b]DAC DB1[LCD DB1
OXFF OXFF PO OXFF OXFF PO Modify RAM	ØØ2Ø JMP start P1.0 1 LED 01Seg. a[DAC DB0]LCD DB0 P2.7 3 %7 JADC DB7 P2.6 1 % (JADC DB7 P2.6 1 % (JADC DB7 P2.6 1 % (JADC DB7
Data Memory addr 0x00 0x00 value 0 1 2 3 4 5 6 7 8 9 A B C D E F	P2.5 1 SW 5[ADC DB5 + P2.4 1 SW 4[ADC DB4
00 1 2 3 4 5 6 7 6 9 A B C D E r 00 1B 00 1B 00 1B 00 0A 00 3A 00 3A 00 10 35 00 35 00 35 00 35 00 35 00 35 00 35 00 35 00	;;;;DELAY FUNCTION P2.3 1 SW 31ADC DB3 ØØ22[TEMPO: NOP P2.2 1 SW 21ADC DB2 P2.1 TEMPO: NOP P2.1 1 SW 21ADC DB3
10 35 00 35 00 35 00 35 00 35 00 35 00 35 00 35 00 35 00 20 35 00 35 00 35 00 35 00 35 00 35 00 35 00 35 00 30 35 00 35 00 35 00 35 00 35 00 35 00 35 00 35 00	ØØ23 NOP P2.0 1 SW 0[ADC DB0 ØØ24 NOP P3.7 1 ADC RD[Comparator Output
40 35 00 30 00 30	ØØ25 NOP P3.6 1 ADC WR ØØ25 NOP P3.5 1 Motor Sensor
60 35 00 35	ØØ27 NOP P3.3 1 AND Gate Output/Display-set 0 ØØ27 NOP P3.2 1 ADD INTR
Copyright ©2005-2012 James Rogers Remove All Breakpoints	
DI / LD 1 2 3 AND Gate Dis	
4 5 6 Key Bounce Di 7 8 9	input input
7 6 5 4 3 2 1 0 * 0 # Standard	
4.82 V output Scope Errorl Function set not called. /	Motor Enabled
DAC	
1	

Red and green continuously on

Syst	tem Clock UF	(MHz) 1	2.0	5	Update Freq.	Time:	tep Pause New Load Swee Copy Paste X 308us - Instructions: 230	P0.7 1 P0.6 1 P0.5 1	Display-select Decoder CS DAC WR Keypad Column 2 Keypad Column 1
R/0	W/O	THO TL		0x00	B 0x00	4	<u>ه</u>		Keypad Column 0
0x00	0x00	0x00 00x0		0x1B	ACC 0x00		^	P0.3 1 P0.2 1	Keypad Row 3 Keypad Row 2
RXD	TXD			0x00	PSW 0x00	ØØ16	L2: MOV P1, #ØDEH	P0.1 1	Keypad Row 1
1	1	TMOD ON	100 R4	0x1B	IP 0x00			P0.0 1	Keypad Row 0
SCON	0x00	TCON 0a	00 R3	0x00	IE OxOO	ØØ19	CALL TEMPO	P1.7 1	LED 7[Seg. dp[DAC DB7[LCD DB7
			R2	0x1B	PCON 0x00			P1.6 1 P1.5 1	LED 6 Seg. g DAC DB6 LCD DB6 LED 5 Seg. f DAC DB5 LCD DB5
pins	bits	TH1 TL	.1 R1	0x00	DPH 0x00	ØØ1B	MOV P1, #ØEEH	P1.4 1	LED 11Seg. eIDAC DB11LCD DB1
OxFF	OxFF P3	0x00 0x0	00 R0	0x1B	DPL 0x00			P1.3 0	LED 31 d1DB31DB31 RS
OxF9	OxFF P2	PC	8051		SP 0x11	ØØ1E	CALL TEMPO	P1.2 0	LED 2] a]DB2]DB2 LCD E
OxF3	OxF3 P1	0x000	5 ACC	00	0 0 0 0 0			P1.1 1 P1.0 1	LED 1 Seg. b DAC DB1 LCD DB1 LED 0 Seg. a DAC DB0 LCD DB0
OxFF	OxFF PO	04000	ALC			aa2a1	JMP start	P2.7 1	SW 7 ADC DB7
				Modif	Y RAM		:Mov pl, #Ø	P2.6 1	SW 61ADC DB6
	Data Mem	y	addr	0x0	0 0x00 value		, HOV P1, WP	P2.5 1	SW 5 JADC DB5
	0 1 2	3 4 5	6 7 8	9 A	BCDEF		:::DELAY FUNCTION	P2.4 1 P2.3 1	SW 4 ADC DB4 SW 3 ADC DB3
00	1B 00 1B	00 1B 00	18 00 OA	00 3A (0 3A 00 3A 00	a a 0 0 1		P2 2 0	
10	3A 00 05	00 35 00	35 00 35	00 35 (0 35 00 35 00		TEMPO: NOP	P2.1 0	SW 1 ADC DB1
20	35 00 35	00 35 00	35 00 35	00 35 0	0 35 00 35 00	ØØ23		P2.0 1	SW 0 ADC DB0
30	35 00 35	00 35 00	35 00 35	00 35 0	0 35 00 35 00	ØØ24		P3.7 1 P3.6 1	ADC RD1Comparator Output
					0 35 00 35 00	ØØ25	NOP	P3.6	ADC WR Motor Sensor
					10 16 00 35 00	ØØ26	NOP	P3.4 1	Display-select Input 1
					0 35 00 35 00	ØØ27	NOP	P3.3 1	AND Gate Output Display-set 0
70	35 00 35	00 35 00	35 00 35	00 35 (0 35 00 35 00	ØØ28	NOP	P3.2 1	ADC INTR
Copyrigh	t @2005-2012	James Roge	ers	Remove	All Breakpoints	ØØ291	NOP	P3.1 1 P3.0 1	Motor Control Bit 11Ext. UART Rx Motor Control Bit 01Ext. UART Tx
		е	10	1 2 4 5 7 8 * 0	3 AND Gate Disat 6 Key Bounce Disa 9 Standard V	abled	U No Parity S-bit UART @ 4300 Baud ~ Rx Rx Reset Tx Tx Tx Send		0.0 V input input int int int int int int int in

Normal operation

System Clock (M2) 5 Update Freq. SUF V/O THO TLO R7 0x00 0x00 0x00 0x00 0x00 R6 0x18 Acc 0x00	RST Step Run New Load Save Copy Paste X Time: 705us - Instructions: 510 U \$4. \$4. \$4. \$4. \$4. \$4. \$4. \$4.	P0.7 1 Display-select Decoder CSIDAC VR P0.6 2 Reynal Colume 2 P1.5 Reynal Colume 1 P1.5 Reynal Colume 0 P0.5 Kaynad Boro 3 P0.5 Kaynad Boro 2
RXD TXD R5 0x00 PSW 0x00 1 1 TMOD 0x00 R4 0x1B P 0x00 SCON 0x00 TCON 0x00 R3 0x00 IE 0x00 pins bits 0x1D PCON 0x00 R4 0x1B PCH 0x00 0xFF 0xFF 0x00 0x00 0x00 0x1B DPL 0x00 0xFF 0xFF 0x00 0x00 0x1B DPL 0x00 0xFF 0xFF 0xFF 0x00 0x00 0000 0000	2019 (CALL TEMPO 2019 CALL TEMPO 2010 NOV 91.492EH 2012 CALL TEMPO 2022 JUP start	Pic. 1 Polyand Rev. 1 Pic. 2 Respect Proc. 1 Pic. 4 LED 618-92, 918AC 08614.020 086 Pic. 4 LED 618-92, 918AC 08614.020 086 Pic. 5 LED 618-92, 918AC 08614.020 086 Pic. 4 LED 618-92, 918AC 08614.020 086 Pic. 5 LED 518-92, 918AC 08614.020 086 Pic. 7 LED 518-92, 918AC 08614.020 086
Data Hemory Image: block of the state of th	<pre>;Hov pl, ## ;Hov pl, ## ##################################</pre>	P2-6 T OM 6 (ADC D065 T P2-8 SM 6 (ADC D065 T T P2-8 SM 6 (ADC D085 T T P2-8 SM 6 (ADC D085 T T P2-9 O SM 6 (ADC D085 T P2-7 ADC D01 (D005 T T P2-8 ADC D01 (D005 T T P2-9 ADC T T T P2-0 ADC T T T
Di 1 2 3 AND Gate Disable 4 5 6 7 6 5 4 7 6 5 4 7 6 7 4 82 vurput Scope 5 6 7 DAC DAC Errort Function set not called 7		MAX input input input input input input input input input input input input input input input input input input



Conclusion-

This lab was effective in showing how to write code using the external I/O pins

connected to the LED and push buttons.

Program-

;;TRAFFIC LIGHTS

Start:

mov p1,#0F3H CALL TEMPO JNB P2.2, NOURG CALL URG NOURG: JB P2.1,FJ JMP START FJ: CALL TEMPO MOV P1, #0F5H CALL TEMPO mov p1, #0f6h call tempo L2: MOV P1, #0DEH CALL TEMPO MOV P1,#0EEH CALL TEMPO mov p1, #0f6h call tempo JMP start ;Mov p1, #0 ;;;;DELAY FUNCTION TEMPO: NOP NOP NOP NOP NOP NOP NOP NOP RET ;;;;URGENT ROUTINE URG: mov p1, #0f6h call tempo mov p1, #0FFh call tempo JNB P2.2, NOURG call urg RET jmp start